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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/765,702	01/26/2004	Jan Vet	23255-08789	1407
758	7590	09/22/2005	EXAMINER	
FENWICK & WEST LLP SILICON VALLEY CENTER 801 CALIFORNIA STREET MOUNTAIN VIEW, CA 94041			GRANT, ROBERT J	
			ART UNIT	PAPER NUMBER
			2838	

DATE MAILED: 09/22/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/765,702

Applicant(s)

VET, JAN

Examiner

Robert Grant

Art Unit

2838

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 26 January 2004.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-17 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-17 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 26 January 2004 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☒ All b) ☐ Some * c) ☐ None of:
1. ☒ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☒ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date 5-21-04 (1page)
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

2. Claims 1-15 are rejected under 35 U.S.C. 102(b) as being anticipated by Kagawa et al. (US 6,211,681).

As to Claim 1, Kagawa discloses a method for determining the available power capacity of an electric power supply (Figure 1 element 10) connected to an arrangement comprising one or more electronic circuits, characterized in that the secondary electrical behavior of one or more electronic circuits fed by the power supply, such as electronic components and integrated circuits, is analyzed (Figure 1, elements 10 and 70).

As to Claim 2, Kagawa discloses a method according to claim 1, characterized in that the secondary behavior of one or more of said electronic circuits in response to the temporary reduction of the supply voltage being applied thereto by the power supply is analyzed (Column 3, lines 9-30).

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As to Claim 3, Kagawa discloses a method according to claim 1, characterized in that the secondary behavior of one or more of said electronic circuits in response to the temporary reduction of the supply current being fed thereto by the power supply is analyzed (Column 3, lines 9-30).

As to Claim 4, Kagawa discloses a method according to claim 2, characterized in that the supply voltage or the supply current is reduced in steps (Column 21, lines 65-67 and column 22, lines 1-5).

As to Claim 5, Kagawa discloses a method according to any one or more of the preceding claims, characterized in that said secondary behavior comprises the resetting or switching to an initial state of one or more of said electronic circuits (Column 5, lines 20-34).

As to Claim 6, Kagawa discloses a method according to any one or more of the preceding claims, characterized in that the secondary electrical behavior is activated, determined and analyzed by a suitably programmed, processor-controlled processing unit (Column 50, lines 66-67 and column 51, lines 1-3).

As to Claim 7, Kagawa discloses an arrangement comprising one or more electronic circuits to be fed by an electric power supply (figure 1), among which electronic components and integrated circuits (element 60), and means for determining the

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available power capacity of the power supply (element 10), characterized in that said means for determining the available power capacity are arranged for analyzing the secondary behavior of one or more of said electronic circuits (elements 10 and 70).

As to Claim 8, Kagawa discloses an arrangement according to claim 7, characterized in that said means for determining the available power capacity of the power supply are arranged for activating, determining and analyzing the secondary behavior of one or more of said electronic circuits (Column 3, lines 9-30).

As to Claim 9, Kagawa discloses an arrangement according to claim 7 wherein said means for determining the available power capacity of the power supply are arranged for temporarily reducing the supply voltage of one or more of said electronic circuits (column 3, lines 9-30).

As to Claim 10, Kagawa discloses an arrangement according to claim 7, characterized in that said means for determining the available power capacity of the power supply are arranged for temporarily reducing the supply current of one or more of said electronic circuits (column 3, lines 9-30).

As to Claim 11, Kagawa discloses an arrangement according to claim 9, characterized in that said means for determining the available power capacity of the power supply are

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arranged for producing a signal as soon as the available power capacity of the voltage supply is lower than a threshold value (Column 4, lines 32-41) .

As to Claim 12, Kagawa discloses an arrangement according to claim 10, characterized in that said means for determining the available power capacity of the power supply comprise a series circuit of at least one resistor and a controllable semiconductor switching element (Column 5, lines 59-64).

As to Claim 13, Kagawa discloses an arrangement according to claim 7, characterized in that at least one of said electronic circuits is a circuit for resetting or switching the arrangement to an initial state (Column 5, lines 20-34).

As to Claim 14, Kagawa discloses an arrangement according to claim 7, characterized in that said means for determining the available power capacity of the power supply comprise a suitably programmed processor-controlled processing unit (Column 50, lines 66-67 and column 51, lines 1-3).

As to Claim 15, Kagawa discloses an arrangement according to claim 14, characterized in that said processing unit forms part of the electronic circuit or circuits to be fed by the power supply (Figure 1, element 110).

Claim Rejections - 35 USC § 103

3. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

4. Claims 16-17 are rejected under 35 U.S.C. 103(a) as being unpatentable over Chou (US 6,363,320) in view of Kagawa et al. (US 6,211,681).

As to Claim 16, Chou discloses a tracking and telemetry system comprising at least one transmitter and at least one receiver (Column 6, lines 31-33), which transmitter is arranged for producing a signal that identifies the transmitter (Column 7, lines 53-54), and which receiver is arranged for receiving said signal (Column 3, lines 50-52). Chou does not expressly disclose the device according to claim 7. Kagawa discloses the device according to claim 7 (see rejection for claim 7). It would have been obvious to a person having ordinary skill in the art to use the power supply device of Kagawa with the tracking and telemetry system of Chou, so that power is managed efficiently and power is not wasted on the non-essential systems when the power supply is in a state where the available power is limited.

As to claim 17, Chou in view of Kagawa further disclose a transmitter (Column 6, lines 31-36) for use in a tracking and telemetry system according to claim 16 (see rejection for claim 16).

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Robert Grant whose telephone number is 571-272-2727. The examiner can normally be reached on M-F 8:30-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Michael Sherry can be reached on 571-272-2084. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

RG

Handwritten signature of Michael Sherry, dated 9/18/05.

MICHAEL SHERRY
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800